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TOWNSEND and TOWNSEND and CREW LLP

By: J. Kuey

04/00
Dated
1/22/06

PATENT
Appl. No. 10/041,033
Amtd. dated January 30, 2006
Amendment under 37 CFR 1.116 Expedited
Procedure Examining Group 2187

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jin YANG et al.

Application No.: 10/041,033

Filed: December 28, 2001

For: SYSTEM AND METHOD FOR
EFFICIENTLY PERFORMING
MEMORY INTENSIVE
COMPUTATIONS INCLUDING A
BIDIRECTIONAL
SYNCHRONIZATION MECHANISM
FOR MAINTAINING CONSISTENCY
OF DATA

Customer No.: 51206

Confirmation No.

Examiner: Kimberly N. McLean Mayo

Technology Center/Art Unit: 2187

**AMENDMENT UNDER 37 CFR 1.116
EXPEDITED PROCEDURE EXAMINING
GROUP 2187**

Entered ref RCE
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action mailed November 30, 2005 for the above-referenced application, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.

OID-2005-246-01